

Department of Computer Science Institute of System Architecture, Operating Systems Group

# EXERCISE 1: GETTING STARTED

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# AGENDA

- first contact with a microkernel OS
- getting to know QEMU
- compile Fiasco
- compile minimal system environment
- talk about system booting
- the usual "Hello World"
- review some stuff and play with the system



QEMU

- developing your own kernel usually requires a dedicated machine
- we will use a virtual machine
- QEMU is open-source software providing a virtual machine by binary translation
- it emulates a complete x86 PC
- available for other architectures as well
- our QEMU will boot from an ISO image

### Setup

- download the source tarball from http://os.inf.tu-dresden.de/Studium/ KMB/WS2010/Exercise1.tar.bz2
- unpack the tarball
  - it comes with a working directory
  - cd in there and have a look around
- initialize the environment with make setup in the toplevel directory you unpacked

### **Test-Driving QEMU**

- create a bootable ISO image
  - create an iso subdirectory for the ISO's content
  - run isocreator from src/l4/tool/bin on this directory
- your ISO will contain a minimal grub installation
- launch QEMU with the resulting ISO: qemu -cdrom boot.iso

### **Compiling the System**

run make within the toplevel directory



# BOOTING



# BIOS

- Basic Input Output System
- fixed entry point after "power on" and "reset"
- initializes the CPU in 16-bit real-mode
- detects, checks and initializes some platform hardware (like RAM, PCI, ATA)
- finds the boot device

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BIOS

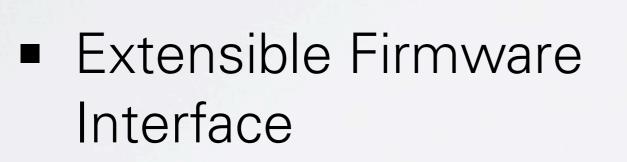


# **BOOT SECTOR**

- first sector on boot disk
- 512 bytes
- contains first boot loader stage and partition table
- BIOS loads code into RAM and executes it
- problem: How to find and boot an OS in 512 bytes?







- plug-ins for new hardware
- no legacy PC-AT boot (no A20 gate)
- built-in boot manager
  - more than four partitions, no 2TB limit
  - boot from peripherals (USB)



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# # MEMORY LAYOUT

#### **BIOS, Video RAM**

**Boot Code** 

#### **Physical Memory**

BIOS

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GRUB

- popular boot loader
- used by most (all?) Linux distributions
- uses a two-stage-approach
  - first stage fits in one sector
  - has hard-wired sectors of second stage files
  - second stage can read common file systems

**Boot Loader** 

BIOS

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GRUB

- second stage loads a menu.lst config file to present a boot menu
- from there, you can load your kernel
- supports loading multiple modules
- files can also be retrieved from network





## GRUB

**Boot Loader** 

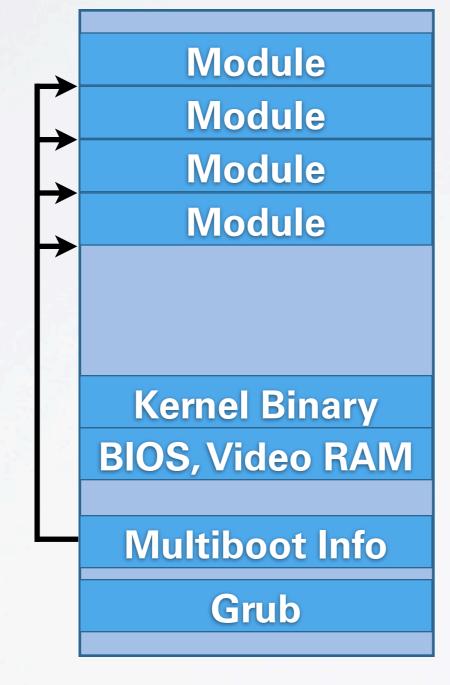
BIOS

- switches CPU to 32-bit protected mode
- loads and interprets the "kernel" binary
- loads additional modules into memory
- sets up multiboot info structure
- starts the kernel

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# # MEMORY LAYOUT



#### Physical Memory

### **Boot Loader**

BIOS



# BOOTSTRAP

- our modules are ELF files: executable and linkable format
- contain multiple sections
  - code, data, BSS
- bootstrap interprets the ELF modules
- copies sections to final location in physical memory

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### BIOS

**Bootstrap** 

**Boot Loader** 



# BOOTSTRAP

- actual kernel is the first of the modules
- must know about the other modules
- bootstrap sets up a kernel info page
  - contains entry point and stack pointer of sigma0 and moe
- passes control to the kernel

Bootstrap

**Boot Loader** 

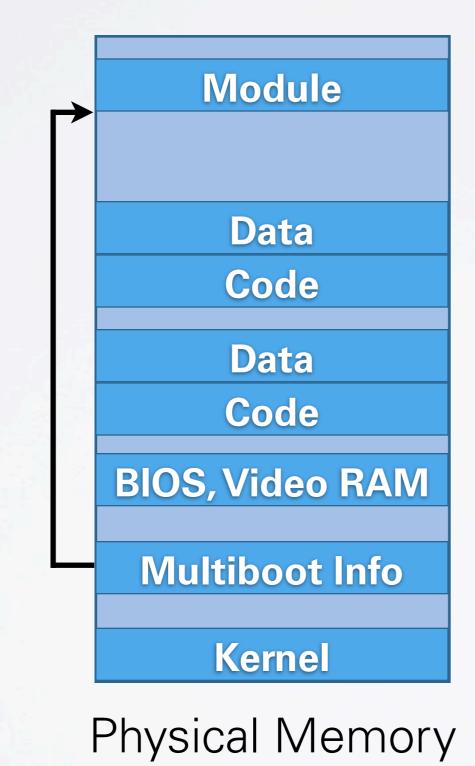
BIOS

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# # MEMORY LAYOUT



### Bootstrap

**Boot Loader** 

BIOS



# **KERNEL LOADER**

- initial kernel code
- basic CPU setup
  - detecting CPU features
  - setup various CPU-tables
- sets up basic page table
- enables virtual memory mode
- runs the actual kernel code

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# MEMORY LAYOUT

#### **Kernel Memory**

Kernel

**Physical Memory** 1:1 mapped

#### Virtual Memory

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**Kernel Loader Bootstrap** 

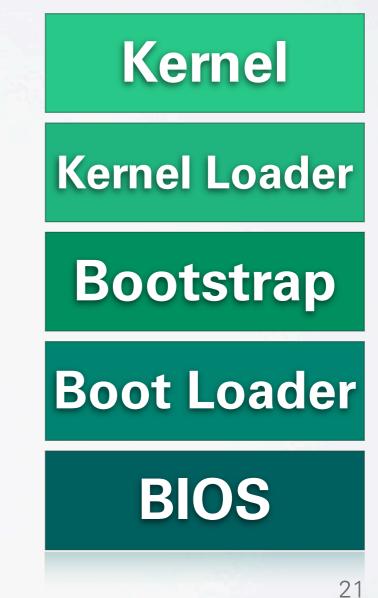
**Boot Loader** 

BIOS



# FIASCO

- sets up kernel structures
- sets up scheduling timer
- starts first pager
- starts first task
- starts scheduling
- scheduler hands control to userland for the first time





# SIGMAO

- is the first pager in the system
- initially receives a 1:1 mapping of physical memory
- ... and other platform-level resources (IO ports)
- sigma0 is the root of the pager hierarchy
- pager for moe

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# MOE

manages initial resources

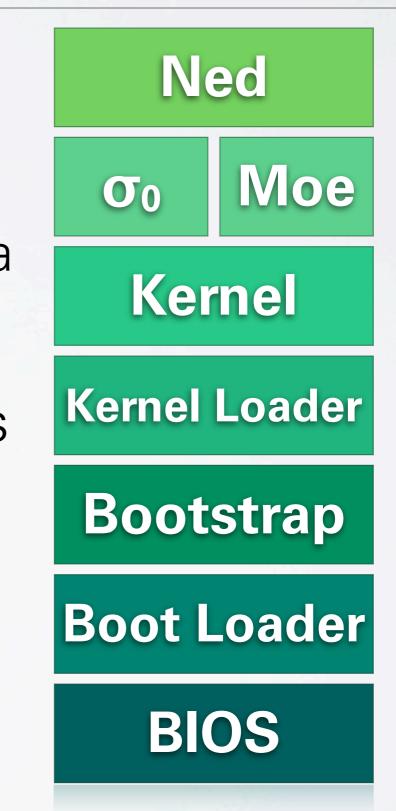
- namespace
- memory
- VESA framebuffer
- provides logging facility
- mini-filesystem for read-only access to boot-modules





NED

- script-driven loader for further programs
  - startup-scripts written in Lua
- additional software can be loaded by retrieving binaries via disk or network drivers
- ned injects a common service kernel into every task



### **Booting Fiasco**

- copy some files to the ISO directory
  - fiasco from the Fiasco build directory obj/fiasco/ia32/
  - bootstrap from obj/l4/x86/bin/x86\_586/
  - sigma0, moe, l4re and ned from obj/l4/x86/bin/x86\_586/l4f/

### **Booting Fiasco**

 edit iso/boot/grub/menu.lst: title Getting Started kernel /bootstrap -modaddr 0x01100000 module /fiasco module /sigma0 module /moe module /l4re module /l4re

rebuild the ISO and run gemu

### **Preparing for Hello**

- create the file hello.lua in the iso directory with this content: L4.default\_loader:start({}, "rom/hello");
- pass ned this new startup script
  - add this line to menu.lst: module /hello.lua
  - pass rom/hello.lua as parameter to moe
- load the future hello module in menu.lst

### **Exercise 1: Hello World**

- create a directory for your hello-project
- create a Makefile with the following content:
   PKGDIR ?= .
   L4DIR ?= path to L4 source tree
   OBJ\_BASE = absolute path to L4 build tree
   TARGET = hello
   SRC\_C = hello.c
   include \$(L4DIR)/mk/prog.mk
- fill in hello.c and compile with make
- run in <u>qemu</u>

### **Exercise 2: Ackermann Function**

- write a program that spawns six threads
  - you can use pthreads in our system
  - add the line
     L4\_MULTITHREADED = y
     to your Makefile
- each thread should calculate one value a(3,0..5) of the Ackermann function:
  - a(0,m) = m+1
  - a(n,0) = a(n-1,1)
  - a(n,m) = a(n-1,a(n,m-1))